

REMARKS

The foregoing amendment does not include the introduction of new matter into the present application for invention. Therefore, the Applicant, respectfully, requests that the above amendment be entered in and that the claims to the present application be, kindly, reconsidered.

The Office Action dated March 25, 2004 has been received and considered by the Applicants. Claims 1-10 are pending in the present application for invention. Claims 1-10 stand rejected by the March 25, 2004 Office Action.

The drawings are objected to because they do not contain descriptive labels. The Examiner states that Figs. 2 and 8 should contain descriptive labels in order to better illustrate the invention. Redlined drawings are submitted with this response that correct the reference signs mentioned by the Examiner in the Office Action.

The Examiner states that 37 CFR 1.77(b) suggests a preferred layout of the specification. The Applicants, respectfully, submit that 37 CFR 1.77(b) does not require that the sections of the specification to a utility application have a section heading. The only requirement is that the sections appear in a particular order. Accordingly, the Applicants, respectfully, decline to make the modification suggested by the Examiner because it is not necessary.

The Office Action rejects Claims 1-10 under the provisions of 35 U.S.C. §102(b), as being anticipated by U.S. Patent No. 5,222,086 issued to Fujita (hereinafter referred to as Fujita). The Examiner states, that regarding Claim 1 Fujita discloses the method in rejected Claim 1 of the present invention. The Examiner further states that Fujita discloses "in the event that the second one of said locations is after and adjacent to the first one of said locations, reading linking information from an end boundary area of said first one of said locations and using the linking information for logically generating the modulated signal," at column 3, lines 40-61 and FIG. 2. The Applicants, respectfully disagree. Column 3, lines 40-61 and FIG. 2 of Fujita discloses links that are used for synchronization. Rejected Claim 1 to the present invention recites "using the linking information for logically generating the modulated signal," which is not disclosed or suggested by Fujita. Accordingly, this rejection is respectfully traversed.

The Examiner further states that regarding Claim 2, that Fujita discloses "in the event that the second one of said locations is before and adjacent to the first one of said

locations, reading linking information from a begin boundary area of said first one of said locations and using the linking formation for logically generating the modulated signal," at column 3, lines 16-39 and FIG. 2. The Applicants, respectfully disagree. Column 3, lines 16-39 and FIG. 2 of Fujita discloses run in blocks to set up a protection of the synchronization block. The run out block is described as being used for interleaving. The Examiner states that Fujita contains linking information. The Applicants respectfully point out that rejected Claim 2 to the present invention recites "using the linking information for logically generating the modulated signal," which is fundamentally different and not disclosed or suggested by Fujita. Accordingly, this rejection is respectfully traversed.

The Examiner further states that regarding Claim 3, Fujita discloses wherein the linking information comprises a last part of the earlier recorded information unit for presetting an error encoder. The Applicants respectfully disagree. There is no disclosure or suggestion within Fujita for the linking information to comprise a last part of the earlier recorded information unit for presetting an error encoder. Accordingly, this rejection is respectfully traversed.

The Examiner further states that regarding Claim 4, Fujita discloses wherein the linking information comprises a first part of the earlier recorded information unit for, after writing the modulated signal, generating an additional modulated signal representing said first part and error correction words based on the at least one information unit and said first part, and recording the additional modulated signal in the begin boundary area. The Applicants respectfully disagree. The cited portions of Fujita disclose run out blocks used for synchronization and detection of the synchronization information. There is no disclosure, or suggestion, for generating an additional modulated signal representing said first part and error correction words based on the at least one information unit and said first part, and recording the additional modulated signal in the begin boundary area as recites by rejected Claim 4. Accordingly, this rejection is respectfully traversed.

The Examiner further states that regarding Claim 5, Fujita discloses wherein the linking information comprises additional recording information, in particular CD subcode. Claim 5 depend from Claim 1, which as previously discussed is believed to be

allowable. Therefore Claim 5 is also believed to be allowable.

The Examiner further states that regarding Claim 6, Fujita discloses wherein linking information is additionally recorded in the end boundary area after the data bytes of the at least one information unit. Claim 6 depend from Claim 1, which as previously discussed is believed to be allowable. Therefore Claim 6 is also believed to be allowable.

The Examiner further states that regarding Claim 7, Fujita at Column 3, line 40 – column 4, line 2, discloses wherein the earlier recorded information unit is terminated by an end boundary area comprising said additionally recorded linking information, and the recording of the modulated signal starts by overwriting the end boundary area, or wherein said record carrier is of a write once type and the linking information includes dummy data bytes of a predefined value for allowing presetting an error encoder when recording a consecutive information unit. The Applicants respectfully disagree. The cited portion of Fujita discusses link positions and states that an overwriting may occur. Fujita does not disclose, or suggest, that earlier recorded information is terminated by an end boundary area having additionally recorded linking information by the modulated signal overwriting the end boundary area, or the linking information including dummy bytes of a predefined value for allowing presetting an error encoder when recording a consecutive information unit. Accordingly, this rejection is respectfully traversed.

The Office Action rejects Claims 8-10 as device claims drawn to the apparatus corresponding to the method of using same as claimed in Claims 1-7, and are rejected for the same reasons of anticipation as used above. The Applicants accordingly traverse this rejection for the same reason as stated above for Claim 1-7.

Claims 11-14 have been added by the foregoing amendment that are of similar scope to claim 3-7. Accordingly Claims 11-14 are believed to be allowable for the reasons states for Claims 3-7.

Applicant is not aware of any additional patents, publications, or other information not previously submitted to the Patent and Trademark Office which would be required under 37 C.F.R. 1.99.

In view of the foregoing amendment and remarks, the Applicant believes that the present application is in condition for allowance, with such allowance being, respectfully, requested.

Respectfully submitted,

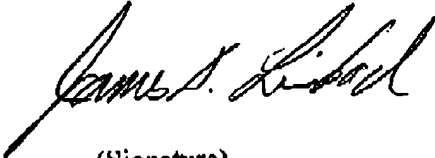
By 

James D. Leimbach, Reg. 34,374
Patent Attorney (585) 381-9983

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